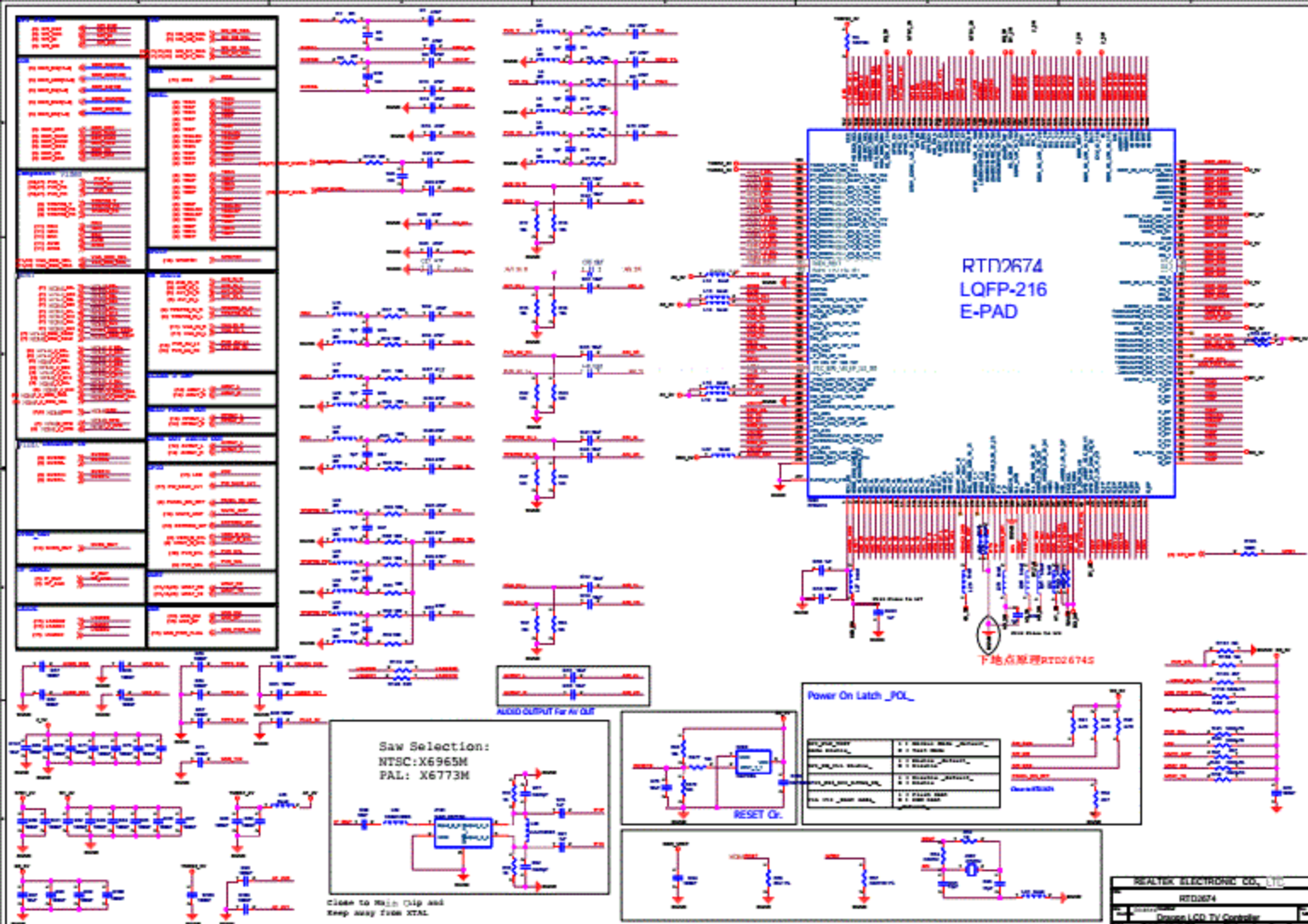
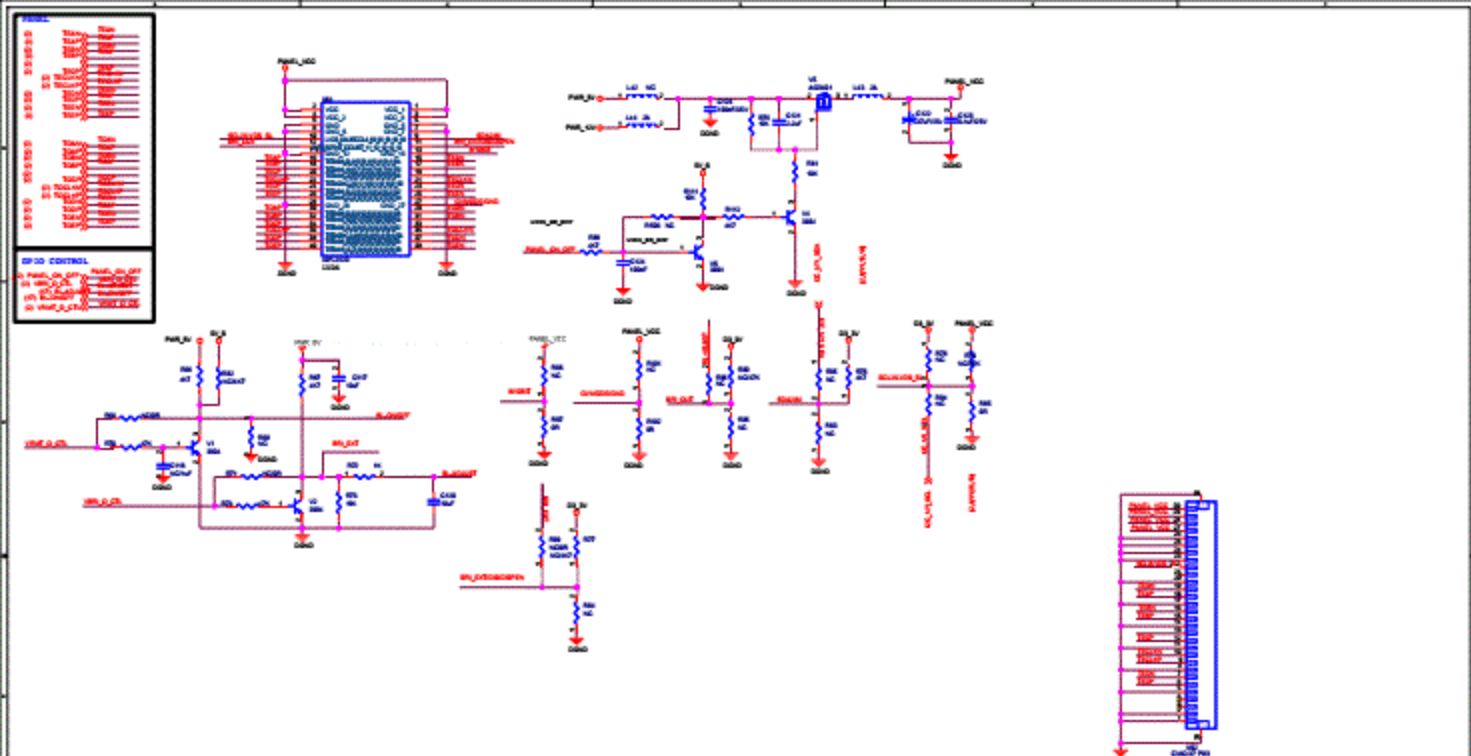
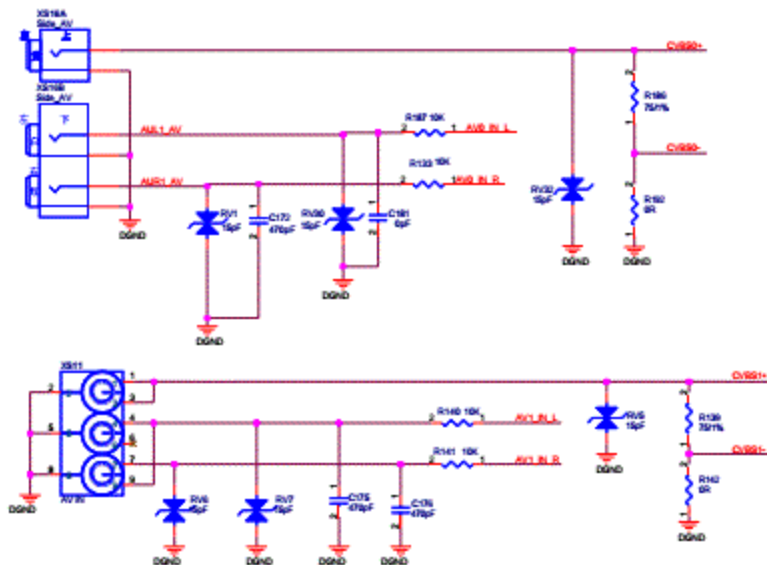






VIDEO DECODER IN

(1) CVBS+	CVBS+
(2) CVBS-	CVBS-
(3) AVI_L_L	AVI_L_L
(4) AVI_L_R	AVI_L_R
(13) CVBS+	CVBS+
(14) CVBS-	CVBS-
(5) AVS_L_R	AVS_L_R
(6) AVS_L_L	AVS_L_L



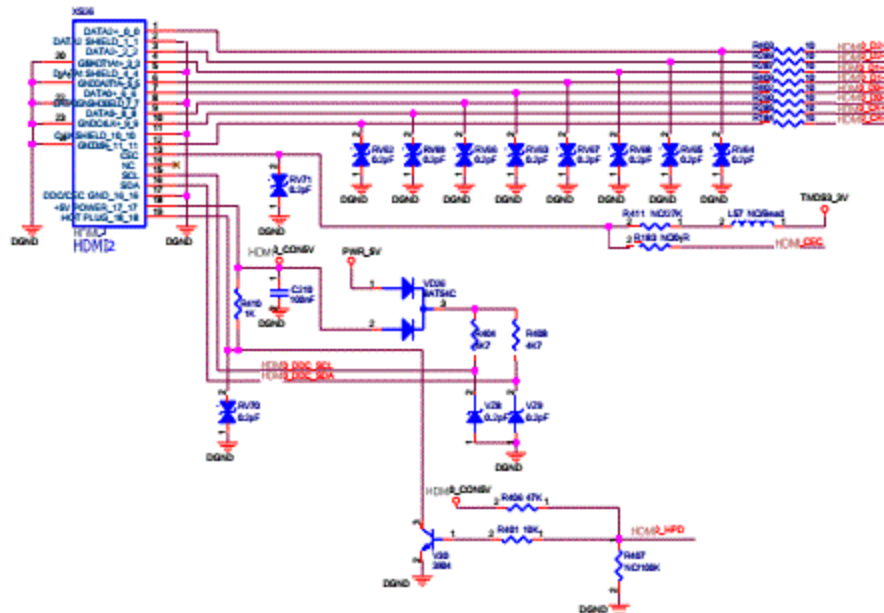
REALTEK ELECTRONIC CO., LTD.

AV/SV IN

Dragon LCD TV Controller

Doc. No.	Doc. Version Number	Rev. 1.00
1000	1.00	1.00

3C04120

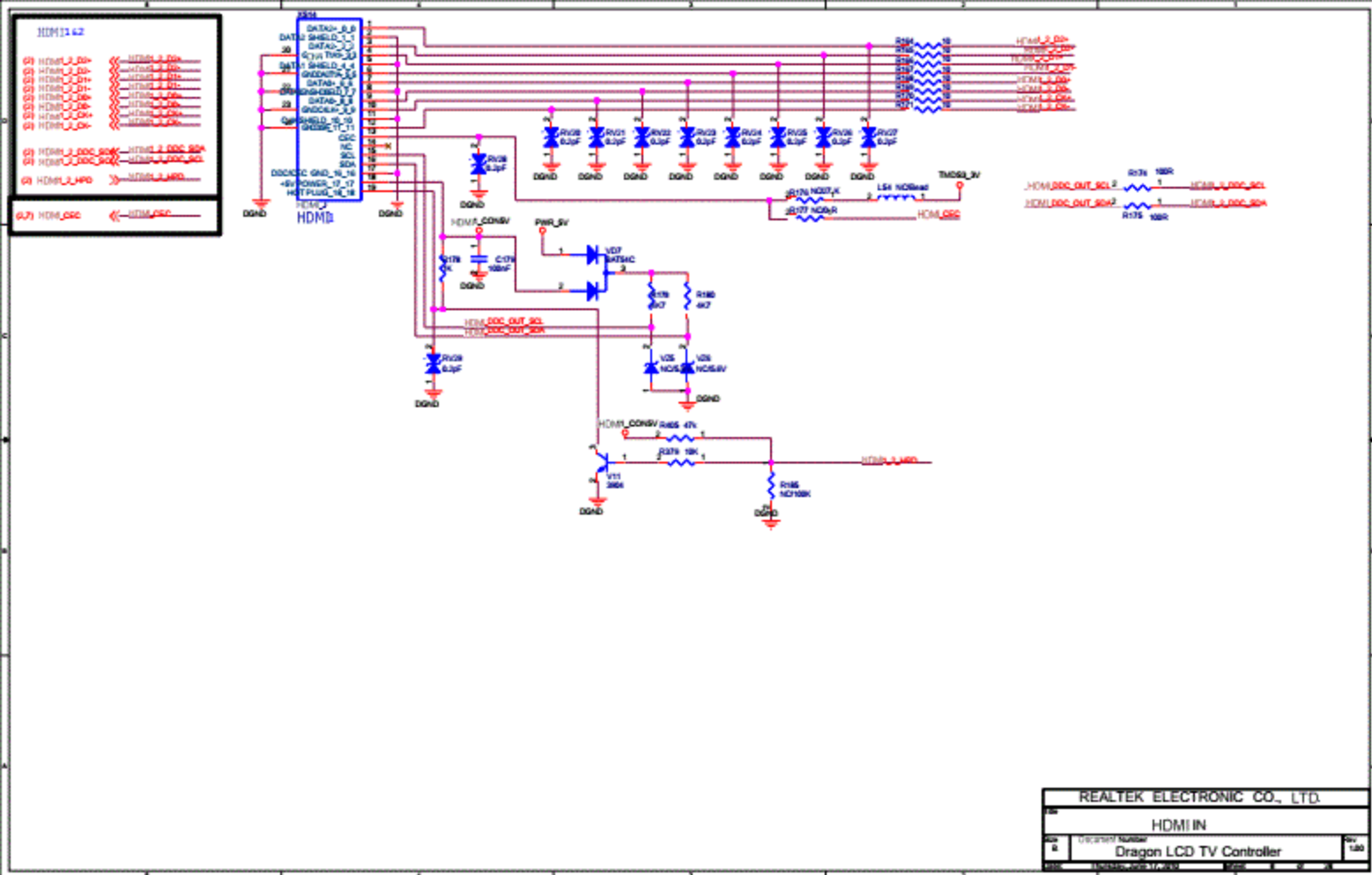


REALTEK ELECTRONIC CO., LTD.

HDMI 0 IN

Dragon LCD TV Controller

Rev: 1.00



FOR RTD2674

(2) PVR_CTL >> PVR_CTL

YPbPr INTERFACE

(9,27) PVR_Y >> PVR_Y
(9,27) PVR_PB >> PVR_PB
(9,27) PVR_PR >> PVR_PR

(13) PVR_AU_L+ >> PVR_AU_L+
(13) PVR_AU_R+ >> PVR_AU_R+

(2,27) DMP_CVBS+ >> DMP_CVBS+
(2) DMP_CVBS- >> DMP_CVBS-

PVR_ON_OFF >> PVR_ON_OFF (28)

AOL >> AOL (28)

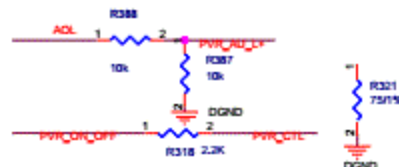
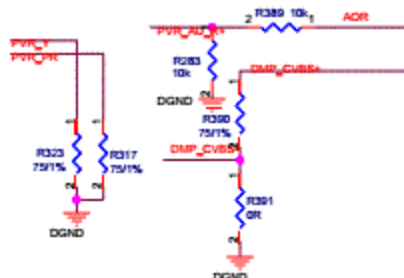
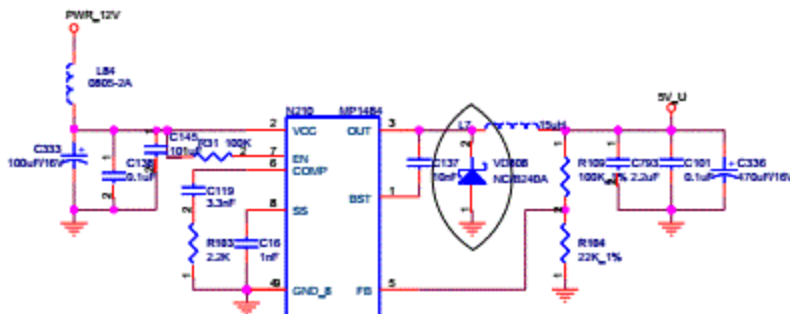
AOR >> AOR (28)

PVR_Y >> PVR_Y (9,27)

PVR_PB >> PVR_PB (9,27)

PVR_PR >> PVR_PR (9,27)

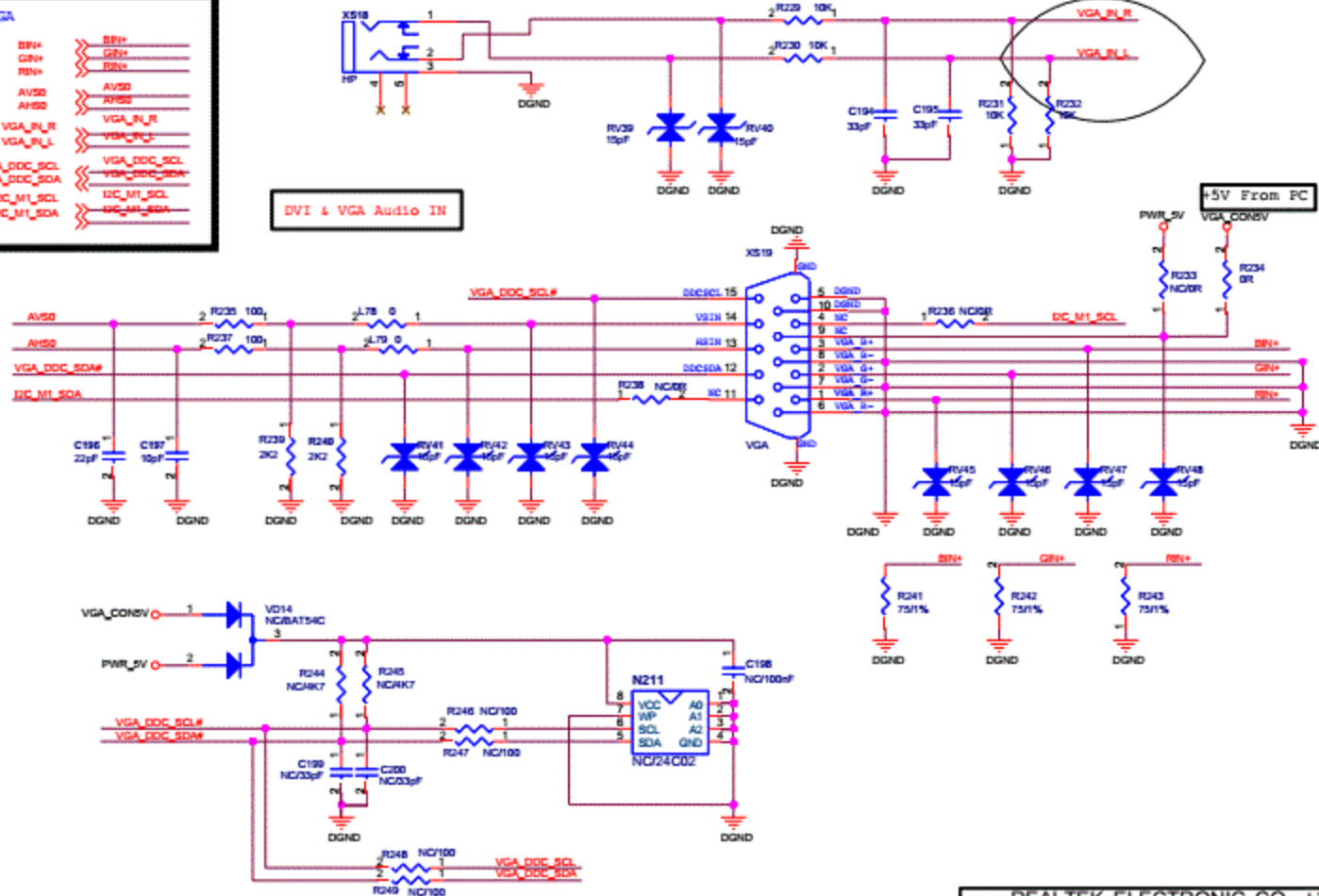
DMP_CVBS+ >> DMP_CVBS+ (2,27)



VGA



DVI & VGA Audio IN

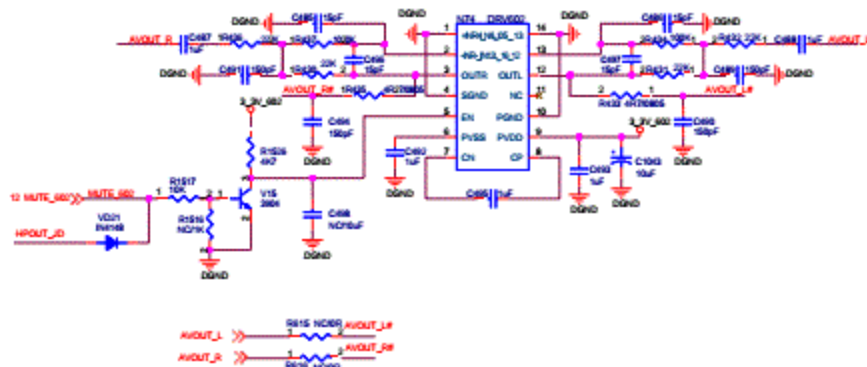
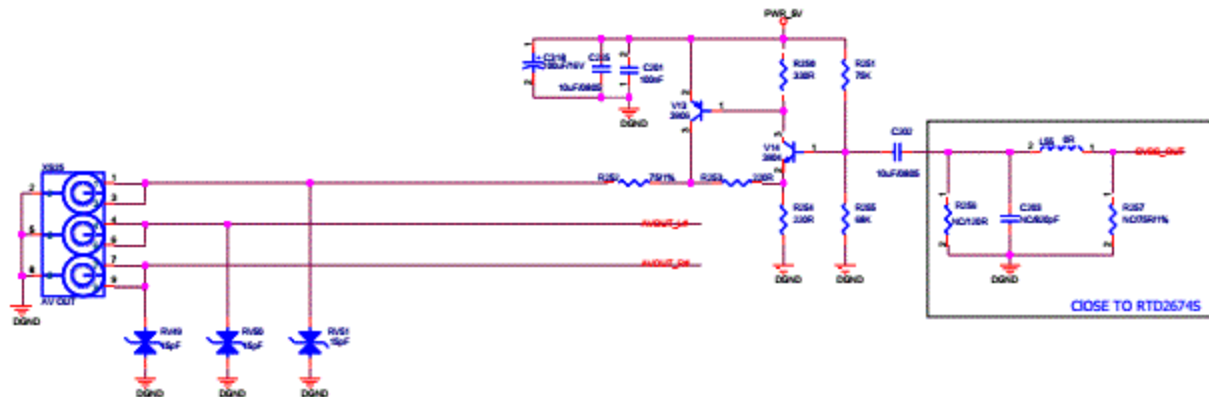


REALTEK ELECTRONIC CO., LTD.

Title		VGA IN	
Size	Customer	Number	Rev
Dragon LCD TV Controller			1.00
Date:	Thursday, June 17, 2010	Sheet	11 of 28

AV OUT

(2) AVOUT_R >> AVOUT_R
(2) AVOUT_L >> AVOUT_L
(2) CVBS_OUT >> CVBS_OUT
(4) HP_OUT_D >> HP_OUT_D

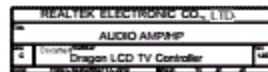


REALTEK ELECTRONIC CO., LTD.

AVOUT

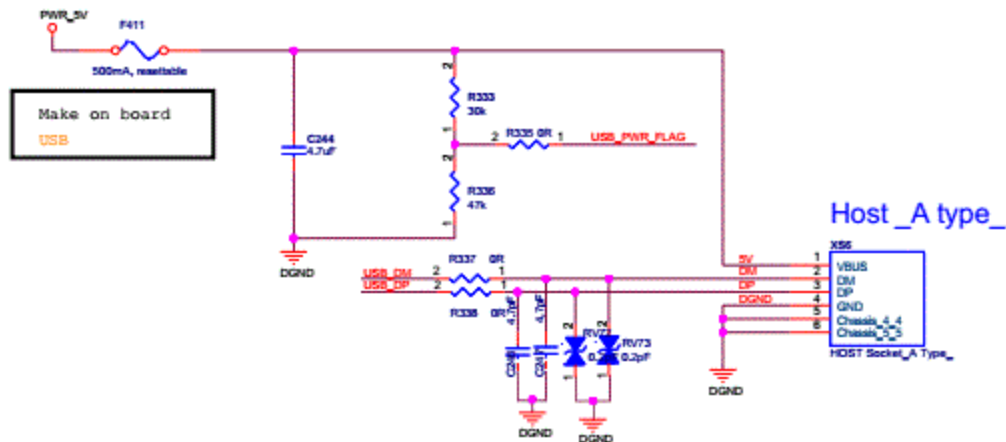
Dragon LCD TV Controller

Rev. 1.00



USB

(2) USB_DM >> USB_DM
(2) USB_DP >> USB_DP
(2) USB_PWR_FLAG >> USB_PWR_FLAG



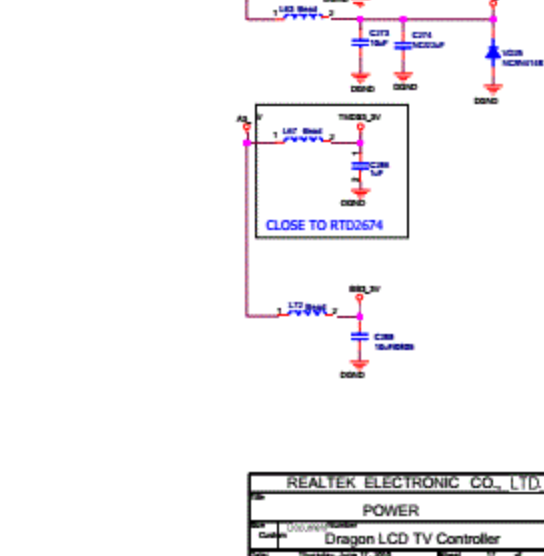
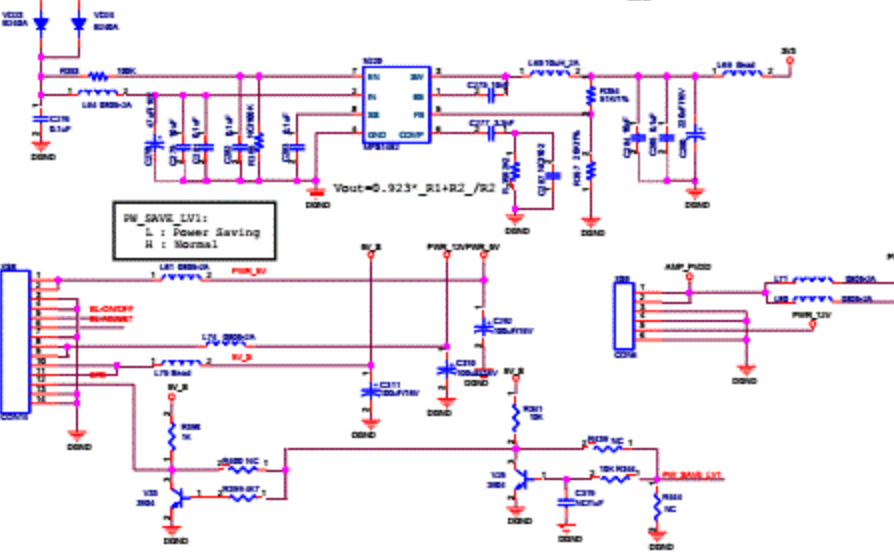
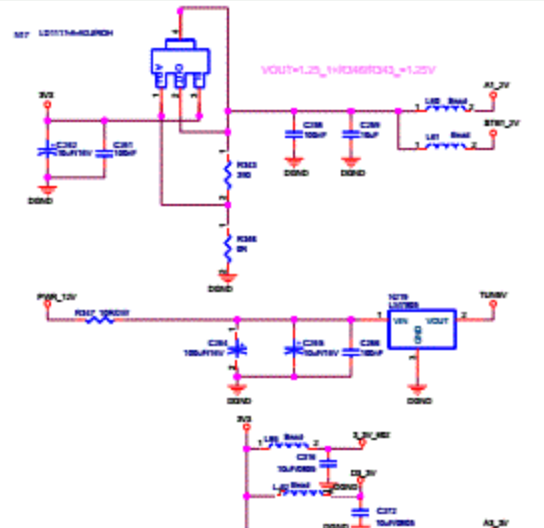
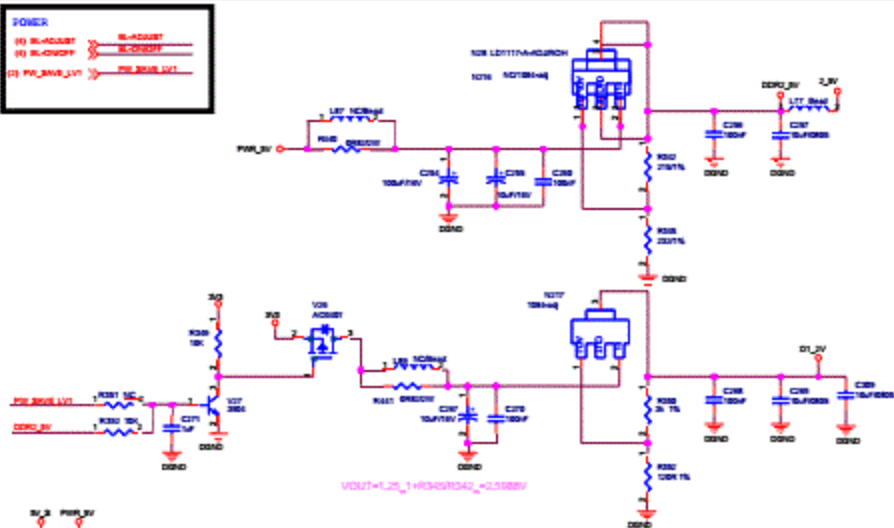
REALTEK ELECTRONIC CO., LTD.

Title			USB
Size	Document Number	Rev	
Cust m	Dragon LCD TV Controller	1.00	
Date:	Thursday, June 17, 2010	Sheet	15 of 28

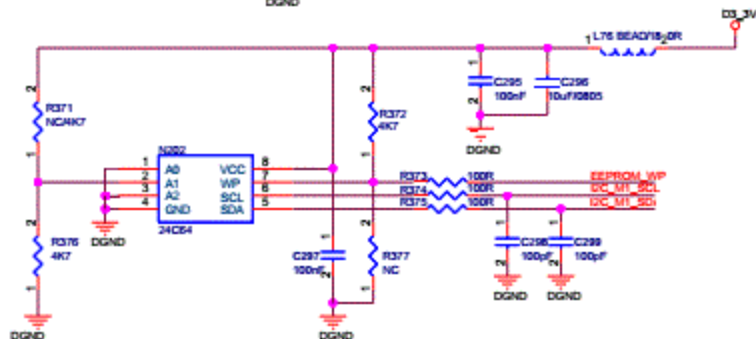
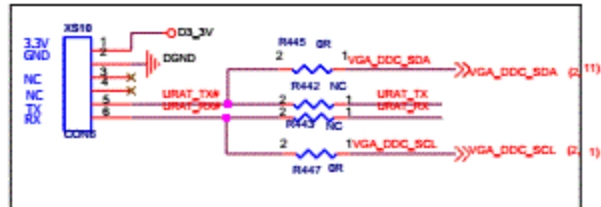
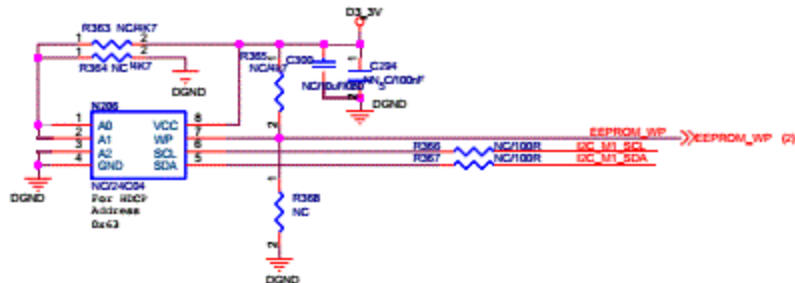
POWER

(H) BL-ADJUST
(H) BL-ONOFF
(H) PW_SAVE_LV1

BL-ADJUST
BL-ONOFF
PW_SAVE_LV1

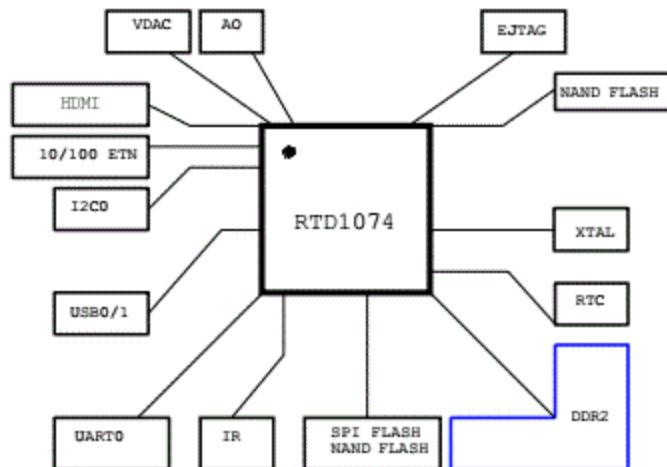


RTC EE/RS232

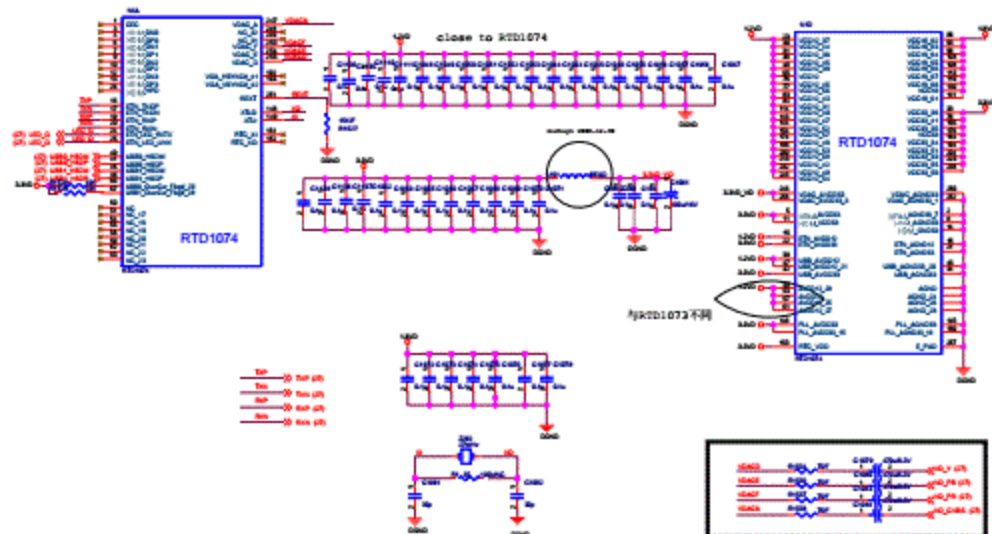


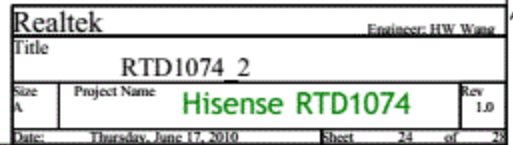
REALTEK ELECTRONIC CO., LTD.

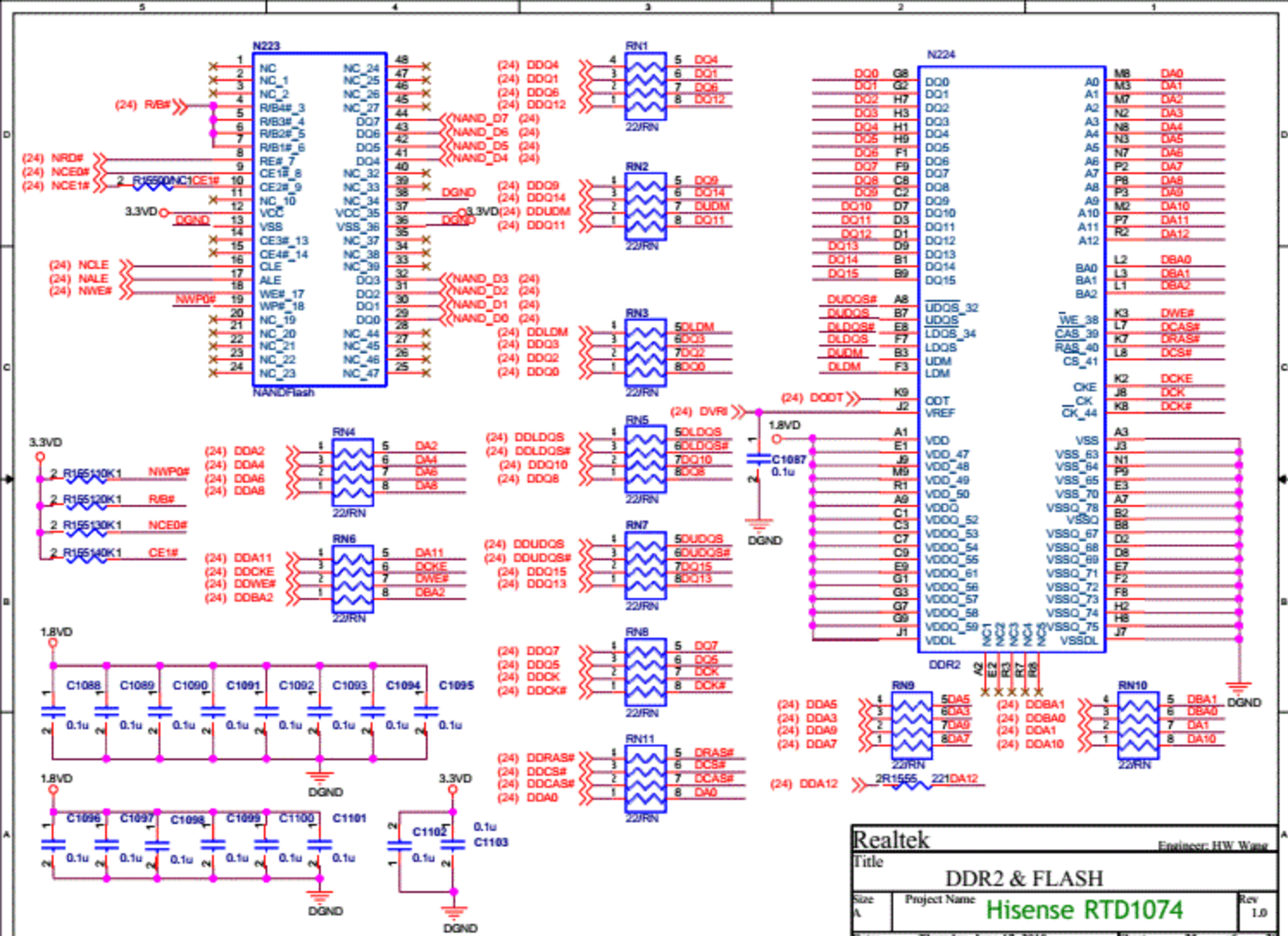
Title		RTC EE/RS232	
Size	Document Number	Dragon LCD TV Controller	
Cur	m	Rev 1.00	
Date	Thursday, June 17, 2010	Sheet	18 of 28

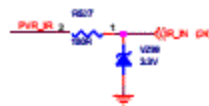


Realtek		Engineer: Shao-Yu Yan	
Title			
FUNCTION BLOCK			
Doc Number	Project Name		Rev 1.0
Hisense RTD1074			
Title: Hisense RTD1074			

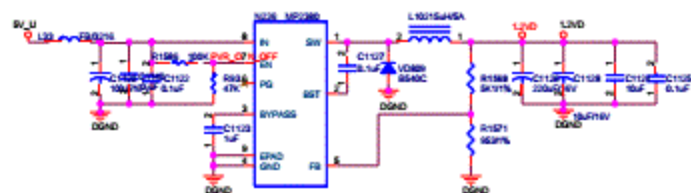








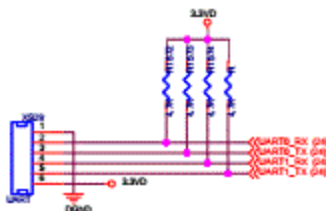
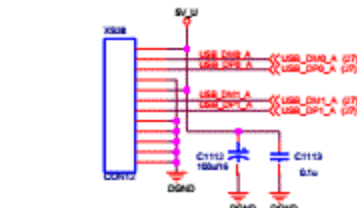
+5V FOR SYS



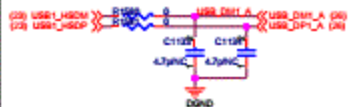
$$R38=R1=10.83 \times \frac{V_{OUT}}{V_{IN}} - 0.923 \text{ k}\Omega \quad (R38=R1=10k)$$

$$V_{OUT}=0.923 \times \frac{V_{IN}}{R1+R2/R2}$$

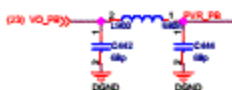
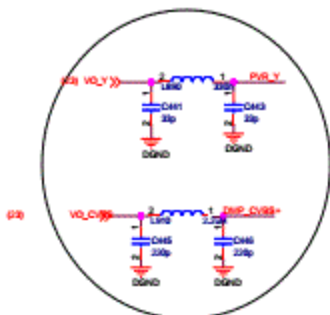
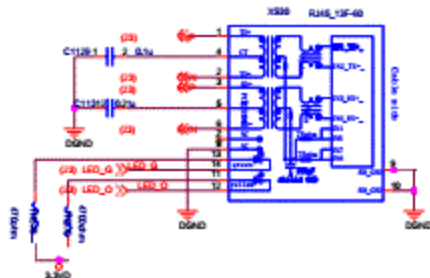
21.10.2008 12:10



Realtek		Realtek USB Modem	
Title		Power & Connectors	
Rev	Project Name	Hisense RTD1074	
0	0	0	
Date		2008.12.10	



AV OUT



PVR_Y << PVR_Y (24)

PVR_FIR << PVR_FIR (24)

PVR_FIR << PVR_FIR (24)

PVR_FIR << PVR_FIR (24)

PVR_FIR << PVR_FIR (24)

Realtek

Engineering 1000000000

File

Video I/O & USB NET

Item

Project Name

RTD1074

Rev

Version

1.0

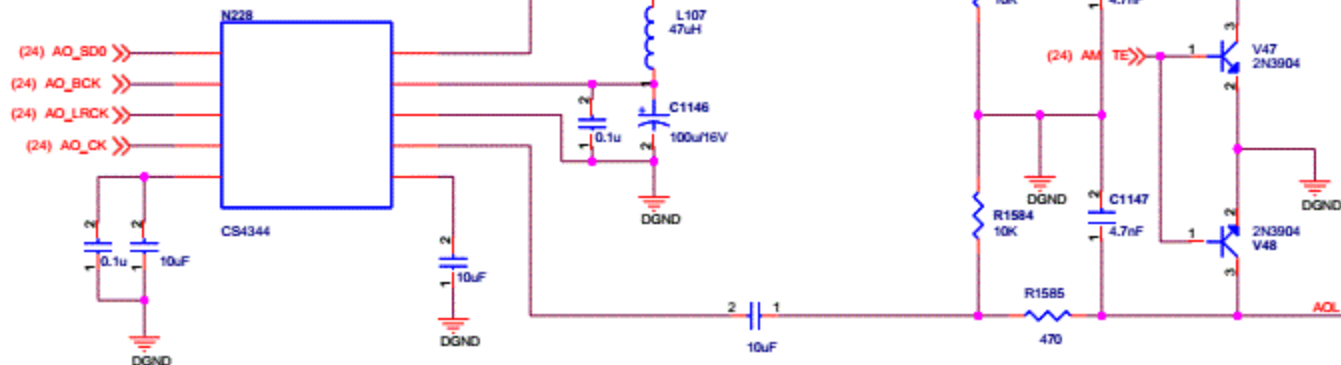
Date

2008-10-10

1.0

AOL (10)
AOR (10)

AOUT



Realtek

Engineer: HW Wang

Title

Audio Output & Buffer

Size

Project Name

Hisense RTD1074
RTD1283-DEMO

Rev

1.0

Date: Thursday, June 17, 2010

Sheet

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of

28

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乙适用于海信 SMT 和 PD

M5 孔/接地 M6 孔/接地 M7 孔/接地 M8 孔/接地 M9 孔/接地地线

A
地线 A

DGND DGND DGND

瑞昱电子有限公司标题块图尺寸定制日期: Document Number om Dragon LCD TV Controller 星期四, 六月 17, 2010

第 1 张, 共 28 张修订版 1.00

5
4
3
2
1
1

C1 47nF

R1 0R

2

1 1 2 TMDS3_3V

SPI 闪存 I2C CVBS1+

VIN11P L2

STB1_2V

STB1_2V

2_5V

SPI_CS#

0R C2 47nF

21

3)SPI_CS#R2 1001

162 21

D1_2V D3_3V

D3_3V

1 22 12

SPI_SCK I2C_M0_SDA PVR_Y

2 年+

C3

3) SPI_SCK

5) I2C_M0_SDA

2_5V

2_5V

116

SPI_DI I2C_M0_SCL

R3

数控/10k

数控

3) SPI_DI

3) SPI_DO

5) I2C_M0_SCL C6 47nF SPI_DO 12 I2C_M1_SDA CVBS1-
COM_A0-L3 5pF C5

4, 8, 11, 15, 18) I2C_M1_SDA

C7 47nF

I2C_M1_SCL

C9 47nF 0R

4, 8, 11, 15, 18) I2C_M1_SCL R6 0R

2

数控 C13 47nF 1 2 2

2

R4 1001

DDR

1

2

12

CVBS0+

COM_Y1-

2

1 1 2 VIN13P

地线

HDMI1_2_DDC_SDA HDMI1_2_DDC_SCL

HDMI0_DDC_SDA#

PANEL_ON_OFF HDMI0_DDC_SCL#

DDR_DQ[15:0]

3) DDR_DQ[15:0]

红外接收

VGA_DDC_SDA VGA_DDC_SCL

PW_SAVE_LV1

HDMI1_2_HPD

21

VRMT_0_CTL

L4

C10

DDR_ADD12

DDR_ADD11 HDMI0_HPD

DDR_DQS1

DDR_ADD[12:0]

DDR_VREF

DDR_ADD9

DDR_ADD8

DDR_ADD7

DDR_ADD6

DDR_ADD5

C8 47nF
DDR_DQ10
DDR_DQ11
DDR_DQ12
DDR_DQ13
DDR_DQ14
DDR_DQ15
红外接收
HDMI_CEC
OR
DDR_DM1 DDR_DQ8
DDR_DQ9
DDR_CKE
3)DDR_ADD[12.0]
15)红外接收
URAT_RX
DDR_CK#
R5 1001
URAT_TX
LSADC1#
LSADC0#
12
SPI_SCK
PVR_Pb PB2+
DDR_CK SPI_CS#
重置#
LSADC2SPI_DO
DDR_BA[1:0]CVBS0-
SPI_DI
1 2 COM_A2-
甘油三酯
3)DDR_BA[1.0]
3)DDR_DQS[1.0]
红外接收
1
控制板
DDR_DQS[1:0]TEAN L6 5pF
4)TEAN
引领
C12
C14 47nF
TEAP 1 2 VIN14P OR
4)技术评估计划地线
R7 1001
2
C19 47nF C15 47nF

OR
2
DDR_DM[1:0]
1
2
TEBN
地线
3)DDR_DM[1.0]
4)四硼酸乙酯
TEBP
4)四乙氧基苯酚
161
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泰康 L8
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4)
DDR_CKE
DDR_RAS#
TECP
R9 100 (3)DDR_CKE (4)TECP 1 22 1 12
TECLKN COM_A2-PVR_Pr PR2+12
TMDS_1.2V_105_162
CECHDDCO_SCLHDDCO_SDA
HDDC1_SCL
HDDC1_SDA
VGA_DDC_SCL
VGA_DDC_SDA
IO_3.3V_97_154
GPIO_C8
GPIO_C6
GPIO_C1
GPIO_C0
STBY_CORE_1.2V_81_139
LSADC4/GPIO_216_138
LSADC2/GPIO_95_137
LSADC1/GPIO_80_136
LSADC0/GPIO_79_135
LSADC_REF
IO_3.3V_77_133
CORE_1.2V_76_132
DQS1

DQ_8
DQ_9
DDR_IO_2.5V_72_128
DQ_10
DQ_11
DQ_12
地址 12
地址 11
地址 9
地址 8
地址 7
地址 6
地址 5
3)DDR_RAS#
3)DDR_CAS#
4)TECLKN DGNDDDR_CAS#
DDR_WE#TECLKP(4)TECLKP
1
TEDN(3)DDR_WE#(4)TEDNDDR_CK
TEDP L9 5pF C18
3) DDR_CK
3)DDR_CK#
4)TEDP C25 47nFDDR_CK#
青少年 R125 0R 0R(4)青少年
2
TOAP C21 10uF HDMI0_CK+
数控
2
R10 100 1
2
1 12
1
2
163
164
108
TEEP DMP_CVBS+VIN10P DDR_ADD4(10, 27)DMP_CVBS+DGND TMD51_2V(4)TEEP ADDR4
107
21
TMD53_3V 2_5VTMD5_3.3V_106_163 DDR_IO_2.5V_210_106
分量视频
10, 27)PVR_Y
165
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TOAN HDMI0_CK-DDR_ADD3C46(4)TOAN P1_RX3N/HDMI_CLKN_1_107_164 ADDR3
166

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 DDR_ADD2PVR_Y (4)TOAP P1_RX3P/HDMI_CLKP_1_108_165 ADDR2
 C29 47nF
 12
 167
 104
 TOBN AV0_IN_R AIN_1R HDMIO_D0+DDR_ADD1PVR_PB (4)TOBN P1_RX2N/HDMI_ON_1_109_166 ADDR1
 10, 27)PVR_PB
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 168
 103
 TOBP DMP_CVBS-
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 10, 27)PVR_PR
 169
 102
 C22 10uF HDMIO_D1+DDR_ADD10(4) P1_RX1N/HDMI_1N_1_111_168 ADDR10
 AIN_3L GPIO_C5
 AIN_2R STBY_CORE_1.2V_92_150
 AIN_2L SPI_DI
 AIN_1R SPI_D0
 AIN_1L SPI_SCK
 AOUT_R SPI_CS_N
 AOUT_L 复位_IN
 HPOUT_R GPIO_C4
 HPOUT_L GPIO_C3
 SCART_FSW_0 GPIO_C2
 USB_AVDD_1.2V_35_37 DQ_13
 I2C0_SCL DQ_14
 I2C0_SDA DQ_15
 IO_3.3V_38_40 DDR_VREF
 GPIO_A0/RF_AGC_39_41 DDR_IO_2.5V_64_120
 GPIO_A1/IF_AGC_40_42 DM1
 CORE_1.2V_41_43 CK#_62_118
 O_FP CK
 O_FN DDR_IO_2.5V_60_116
 O_EP CKE
 1 2 170
 101
 YPBPRO_Y
 TOCP AV0_IN_L AIN_1L HDMIO_D1+DDR_BA1
 9)YPBPRO_Y
 4)TOCP
 P1_RX1P/HDMI_1P_1_112_169 BA1
 171
 100

YPBPRO_PB TOCLKN HDMI0_D2-DDR_BA0
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 9) YPBPRO_PR
 4) TOCLKN
 P1_RX0N/HDMI_2N_1_113_170 BA0
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 21
 172
 99
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 P1_RX0P/HDMI_2P_1_114_171 CORE_1.2V_202_98
 173
 98
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 97
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 P0_RX3P/HDMI_CLKP_0_116_173 CAS#_200_96
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 96
 杜松子酒+
 东昂 HDMI1_2_D0-DDR_WE#10k 10k(11)GIN+(4)东昂
 P0_RX2N/HDMI_0N_0_117_174 WE#_199_95
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 11)AVS0
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 P0_RX1N/HDMI_1N_0_119_176 DDR_IO_2.5V_197_93C26 47nF
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 11, 18) VGA_DDC_SCL11, 18) VGA_DDC_SDA
 14) SPDIF01
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 DQ_3
 DQ_4
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 C27 47nF
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 1 2 182
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 2
 TMDS_1.2V_124_181
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 APLL_GND DDR_IO_2.5V_190_86 DQ_6
 LQFP-216 DQS0
 DQ_7
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 7)HDMIO_CK-
 7)HDMIO_CK+
 水平同步
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 HDMIO_CK+
 HDMIO_D0-AV1_IN_R L13 AVS0 DDR_DQ7bead(6)AV1_IN_R
 6)AV1_IN_L 垂直同步
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 7)HDMI0_D0-
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 HDMI0_D0+YPP3_3V2L15
 7)HDMI0_D0+
 A3_3V D1_2V
 VD_VDD_3.3V_130_187 CORE_1.2V_186_82 TCON/GPIO_B12_185_81 TCON/GPIO_B10_184_80
 电子垫
 C32 47nF
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 82
 HDMI0_D1-YPBPRO_IN_R R15 R16 VGA_B+SPDIF01
 SPDIF00R R17 100
 1
 L14 珠(9)YPBPRO_IN_R
 7)HDMI0_D1-
 _VGA_B_VIN_OP_

 81
 HDMI0_D1+YPBPRO_IN_L RIN+
 VGA_R+VGA_B-VBRI_O_CTL10k 10k(9)YPBPRO_IN_L
 7)HDMI0_D1+
 VIN_ON
 191
 80
 HDMI0_D2-VGA_G+MUTE_AMP
 7)HDMI0_D2-
 _VGA_G_VIN_1P_133 TCON/GPIO_B9_183_79
 1
 192
 79
 HDMI0_D2+VGA_IN_R VGA_G-(11)VGA_IN_R
 7)HDMI0_D2+
 D3_3V
 VIN1_N IO_3.3V_182_78
 193
 78
 HDMI0_DDC_SDA#
 VGA_IN_L L16 5pF C33 VGA_R+(11)VGA_IN_L
 7)HDMI0_DDC_SDA#
 _VGA_R_VIN_2P_135 TCON/GPIO_B8_181_77 R18 4K7C34 47nF
 1
 2
 194
 77

HDMI0_DDC_SCL#VGA_R-I2C_M1_SDA0R D3_3VR19 100
 7)HDMI0_DDC_SCL#
 2
 HDMI1_2_D0-PR1+
 2
 VIN_2N TCON/GPIO_B7_180_76 R20 4K7
 1
 2
 1 12 195 76 1
 2
 PVR_AU_L+
 VGA_R-PB1+I2C_M1_SCLDGNDPVR_AU_L+DGND(13)
 _Pb_VIN_3P_137_194 TCON/GPIO_B6_179_75
 C35 10uF 196
 75
 HDMI1_2_CK-PVR_AU_R+COM_Y0-EEPROM_WP
 8)HDMI1_2_CK-
 13)PVR_AU_R+
 VIN_Y0N TCON/GPIO_B5_178_74
 1 2 197
 74
 HDMI1_2_CK+PVR_AU_R+AIN_3R Y1+
 8)HDMI1_2_CK+
 _Y_VIN_4P_139 TCON/GPIO_B4_177_73
 198
 73
 PVR_CTL
 8)HDMI1_2_D0-
 _Pr_VIN_5P_140_197 TCON/GPIO_B3_176_72
 199
 72
 HDMI1_2_D0+PB2+USB_PWR_FLAG
 L17
 C36 10uF
 8)HDMI1_2_D0+
 _FSC_B/Pb_VIN_6P_141_198 TCON/GPIO_B2_175_71C37 47nF
 200
 71
 HDMI1_2_D1-PVR_AU_L+1 2 AIN_3L COM_Y1-
 OR
 R21 100
 8)HDMI1_2_D1-(8)HDMI1_2_D1+
 VIN_Y1N TCON/GPIO_B0_174_70
 D 级 AMP 杜松子酒+1 2 2
 1 12 201
 70

HDMI1_2_D1+VGA_G+PR2+D1_2V
_FSC_G/Pr_VIN_7P_143_200 CORE_1.2V_173_69
21
第 202 章
69
HDMI1_2_D2-Y2+TEANL18 珠
8)HDMI1_2_D2-
_FSC_R/Y_VIN_8P_144_201 E_AN
1
1
2203
68
HDMI1_2_D2+AOUT_L A1_2V0 TEAP
8)HDMI1_2_D2+(8)HDMI1_2_DDC_SDA
14)AOUT_L
ADC_VDD_1.2V_145_202 E_AP1 2 204
67
HDMI1_2_DDC_SDA
HDMI1_2_DDC_SCL
AOUT_R L20 5pF C38 R22 R23 A1_2V1 TEBN(14)AOUT_R A1_2V
VD_VDD_1.2V_146_203 E_BNC39 47nF
205
66
TEBPNC NCOR L19 珠子
8)HDMI1_2_DDC_SCL
R24 1001
地线车辆识别号 11P
VD_GND E_BP
2
2
HPOUT_L SV_Y+
1
2
12
206
65
DGND
_SV-C/FSC_CVBS_VIN_11P_148_205
拔出
207
64
HDMI_CEC COM_A0-TECP
7, 8)HDMI_CEC
VIN_A0N E_CP
208
63

泰克宁

14)

输出输出_L

14)

输出输出_R

_SV-Y_VIN_9P_150_207 E_DN

209

62

HDMI0_HPD

HDMI1_2_HPD

HPOUT_R SV_C+TECLKP

7)HDMI0_HPD (8)HDMI1_2_HPD

_SV-C_VIN_12P_151_208 E_DP

210

61

COM_A1-TEDN

L21 接地端

VIN_A1N E_EN

C40 47nF

211

60

C41 10uF VIN10P TEDP

OR

R25 100

ADC2X_GND_3.3V_22_25

_CVBS/SV-Y_VIN_10P_153_210 E_EP

CVBS 输出音频输出 BIN+1 22

1 12 12 212

59

VGA_B+YPBPRO_IN_L AIN_4L VIN13P 青少年

USB_AVDD_3.3V_32_34

_CVBS/SV-C_VIN_13P_154_211

_CVBS_VIN_14P_156 AVDD_BB1_3.3V_157_214

E-PAD_212_216

VCM_BBAVDD_BBO_3.3V_5_1AIN_5L/LSADC6_6_2AIO_2R

E_FN

DAC_VDD_3.3V_18_21

AVOUT_1

视频解码器输入

213

PLL_VDD_3.3V_29_32

58

AVOUT_L COM_A2-TEEP(12)AVOUT_L VIN_A2N E_FP

1

214

57

AVOUT_R C43 10uF L22 磁珠 VIN14P
 CORE_1. 2V_30_33
 12) AVOUT_R D3_3VIO_3. 3V_160_5612 12 215
 56
 CVBS0+YPBPRO_IN_R AIN_4R AVDD_BB1 TOANL23 5pF C42 BB3_3V
 9) CVBS0+
 O_ANC44 47nF
 SCART_FSW_1
 216
 55
 CVBS0-TOAP0R R26 100
 9) CVBS0-
 2
 2
 PW_SAVE_LV1 NC NC
 17) PW_SAVE_LV1
 AGND_BB1 O_AP
 21
 GPIO DGND 1
 2
 1 1 2 VGA_B-
 ADC2X_VDD
 CVBS1+LED CVBS1-
 6) CVBS1+(15) LED
 DAC_GND
 PLL_GND AVOUT_2
 217
 R27 R28
 AIO_1R
 AIN_4R
 AIN_3R AIO_2L
 AIO_1L
 AIN_4L
 6) CVBS1-
 高速数据管理
 O_DN
 X 输出
 HSDP
 欧_EN
 氧 DP
 O_CP
 O_BN O_BP IF_N
 IF_P
 鑫地线面板_开_关 L24 N201
 OR R29 100 C45 47nF RTD2674(4) PANEL_ON_OFF
 静音放大器 YPBPRO_Y1 2 2 1 1 2 Y1+DGND

14) 静音_放大器

1

12 2

3

4

5

6

7

AIN_4R 8

9

10

11

AIN_2R 12

13

14

15

16

17 号

18

19

20

21

12 22

L28 珠子 CVBS_OUT 23

24

25

26

27

28

29

30

31

32

33

34

35

36

37

2 38

39

2 R34 4K71 40

41

RF_AGC 42

43

44

45

东园 46

47

48

49

50

51

52

53

54

USB_PWR_CTRL

1

EEPROM_WP

18)EEPROM_WP

L25 5pF

I2C_M0_SDA I2C_M0_SCL DGND VDAC3_3V0

VADC3_3V1 L69 珠 2

L70 珠 2

C47

AVDD_BB0

HPOUT_R HPOUT_L

PVR_SEL

USB_3V3

USB_1V2

CVBS_输出

C48 47nF

L31 珠 USB_DM

VBRI_O_CTL OR

PLL3_3VAOUT_R

USB_DP

L32 珠子

TOCLKN TOCLKPAOUT_L

VD3_3V

R30 100

AIO_2R

AIO_1R2

2

C49 47nF PVR_SEL YPBPR0_PB1 2 2 1 1 2 PB1+C50 1uF OR R32 100

AIN_3R

AIN_1R

4)VBRI_O_CTL

4)VRMT_O_CTL

AIO_2L

AIO_1L

AIN_4L

AIN_3L

AIN_2L